

10,039,612 *cf e 80w*
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March 21, 2005

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Re: **CERTIFICATE OF CORRECTION**
U. S. Letters Patent No. 6,865,646
Issued: March 8, 2005
For: **SEGMENTED DISTRIBUTED MEMORY
MODULE CACHE**
Inventor: David
Our File No. 42390.P13874

Dear Sir:

Enclosed is the Certificate of Correction (two copies) for the above-referenced patent.
This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C.
Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Edwin H. Taylor
Reg. No. 25,129

EHT/jsq
Enclosures

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,865,646
DATED : March 8, 2005
INVENTOR(S) : David

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 9, at line 4, delete "memo" and insert --memory--.
In column 9, at line 21, delete "location" and insert --locations--.
In column 9, at line 23, delete "side" and insert --size--.
In column 9, at line 24, delete "s" and insert --is--.
In column 10, at line 15, delete "size" and insert --size,--.
In column 10, at line 24, delete "memo" and insert --memory--.

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PATENT NO. 6,865,646

Certificate of Correction (PTO Form 1050)-Amended